

DS_DMA - Bug # 67: SP605_LX45T_WISHBONE - Error in TEST_ADM_READ_8KB

Status:	Resolved	Priority:	Normal
Author:	Dmitry Smekhov	Category:	
Created:	03/07/2016	Assignee:	Dmitry Smekhov
Updated:	03/07/2016	Due date:	03/07/2016
Subject:	SP605_LX45T_WISHBONE - Error in TEST_ADM_READ_8KB		
Description:	Current result in the tesst: TEST_ADM_READ_8KB Block 0 - read: 0 00000000 1 00000000 2 00000080 3 00000000 The result should be: Block 0 - read: 0 A5A50123 1 00000000 2 FFFFFFFF 3 FFFFFFFF		

Associated revisions

03/07/2016 03:29 am - Dmitry Smekhov

fixes #67 - correction ctrl_ram_cmd.vhd for Spartan6 (is_dsp48=0)

History

03/07/2016 03:38 am - Dmitry Smekhov

- Due date set to 03/07/2016

- Status changed from Confirmed to Resolved

Correct r53

Component ctrl_ram_cmd.vhd with is_dsp48=0 work incorrect

Process "pr_dsp: process(aclk)" must work with "aclk" signal.