

## DS\_DMA - Development # 65: AC701\_A200T\_CORE

<b>Status:</b>	Resolved	<b>Priority:</b>	Normal
<b>Author:</b>	Dmitry Smekhov	<b>Category:</b>	
<b>Created:</b>	02/16/2014	<b>Assignee:</b>	Dmitry Smekhov
<b>Updated:</b>	09/13/2015	<b>Due date:</b>	09/13/2015
<b>Subject:</b>	AC701_A200T_CORE		
<b>Description:</b>	Project for AC701 Evaluation board		

### History

#### 02/17/2014 12:47 am - Dmitry Smekhov

Revisuon r46 - the project started.

#### 02/18/2014 01:26 am - Dmitry Smekhov

Revision r47 - `Đ'Đ%Đ±Đ°Đ²Đ»ĐμĐ½Ñ' Ñ,,Đ°Đ¹Đ»Ñ'.`

`ĐœĐ%Đ¶ĐμĐ»Đ,Ñ€Đ%Đ²Đ°Đ½Đ,Đμ Đ%Ñ•Ñ,Đ°Đ½Đ°Đ²Đ»Đ,Đ²Đ°ĐμÑ,Ñ•Ñ• Đ½Đ° Đ²Ñ€ĐμĐ¼ĐμĐ½Đ, -5us.`

#### 02/23/2014 01:20 pm - Dmitry Smekhov

`Đ'Ñ•Đμ ĐμÑ%Ñ' Đ½Đμ ÑfĐ'Đ°Ñ'Ñ,Ñ•Ñ• Đ¿Ñ€Đ%Đ¼Đ¼Đ%Đ'ĐμĐ»Đ,Ñ€Đ%Đ²Đ°Ñ,Ñ€Đ ÑfĐ-ĐμĐ» PCI Express. ĐœĐ%Đ¶ĐμÑ, Ñ•Ñ,Đ% Đ,Đ- Đ-Đ° Ñ,Đ%Đ³Đ% Ñ±Ñ,Đ% Ñ•Đ'Ñ€Đ% Ñ•Ñ,,Đ%Ñ€Đ¼Đ,Ñ€Đ%Đ²Đ°Đ½Đ% ISE 14.5 Đ° Đ±Đ,Đ±Đ»Đ,Đ%Ñ,ĐμĐ°Đ, ActiveHDL Đ%Ñ, 14.6 ?`

#### 09/13/2015 11:01 am - Dmitry Smekhov

- Due date set to 09/13/2015

- Status changed from Confirmed to Resolved

- % Done changed from 0 to 100

`Đ"Đ»Ñ• Đ¿Ñ€Đ²Đ,Đ»Ñ€Đ½Đ%Đ¹ Ñ€Đ°Đ±Đ%Ñ,Ñ' Ñ,Ñ€ĐμĐ±ÑfĐμÑ,Ñ•Ñ• ÑfÑ•Ñ,Đ°Đ½Đ%Đ²Đ°Đ° rx_np_req=>'1' Đ¿Ñ€Đ, Đ¿Đ%Đ'Đ°Đ»ÑŽÑ±ĐμĐ½Đ,Đ, cl_a7pcie_x4`

`Đ ĐμĐ²Đ,Đ-Đ,Ñ• r52;`

`ĐŸÑ€Đ%ĐμĐ°Ñ, Đ¿Đ%Đ»Đ½Đ%Ñ•Ñ,Ñ€ĐÑŽ Đ¼Đ%Đ'ĐμĐ»Đ,Ñ€ÑfĐμÑ,Ñ•Ñ•. Đ"Đ»Ñ• Đ-Đ°Đ¿ÑfÑ•Đ°Đ° Đ²Ñ•ĐμÑ... Ñ,ĐμÑ•Ñ,Đ%Đ² Đ,Ñ•Đ¿Đ%Đ»Ñ€Đ-ÑfĐμÑ,Ñ•Ñ• Ñ,,Đ°Đ¹Đ» *src estbenchahdlun_ahdl.tcl*`

`ĐŸÑ€Đ,Đ¼ĐμÑ€Ñ' Đ%Ñ,Ñ±Ñ'Ñ,Đ%Đ² Đ'Đ»Ñ• Đ¿Ñ€Đ²Đ,Đ»Ñ€Đ½Đ%Đ³Đ% Ñ•ĐμĐ°Đ½Ñ•Đ° Đ¼Đ%Đ'ĐμĐ»Đ,Ñ€Đ%Đ²Đ°Đ½Đ,Ñ• Đ-Đ'ĐμÑ•Ñ€: *src estbenchahdllog_example*`

`ĐŸĐ%Đ»Đ½Ñ'Đ¹ Đ°Ñ€Ñ...Đ,Đ² Đ¿Ñ€Đ%ĐμĐ°Ñ,Đ° Đ'Đ»Ñ• Active-HDL 9.3 Đ¼Đ%Đ¶Đ½Đ% Ñ•Đ°Đ°Ñ±Đ°Ñ,Ñ€Đ Đ-Đ'ĐμÑ•Ñ€: *http://ds-dev.ru/projects/ds-dma/wiki/Download*`

`ĐŁ Đ¼ĐμĐ½Ñ• Đ½ĐμÑ, AC701, Đ½Đ% Ñ•Ñ,Đ%Ñ, Đ¿Ñ€Đ%ĐμĐ°Ñ, Đ¿Ñ€Đ%Đ²ĐμÑ€Đμ½ Đ½Đ° Đ¼Đ%Đ'ÑfĐ»Ñ•Ñ... FMC123E, FMC124P, FMC125cP;`